



Košice Slovak Republic 2000

BEHAVIOUR OF ARC WELDER WITH HIGH FREQUENCY LCC RESONANT CONVERTER

Peter Dzurko, Jaroslav Dudrík, Peter Višnyi

Department of Electrical Drives and Mechatronics, Faculty of Electrical Engineering and Informatics, Technical University of Košice, Letná 9, 042 00 Košice, Slovak Republic tel.: 00421 95 602 2254; fax: 00421 95 633 0115; e-mail: dzurko@tuke.sk, dudrik@tuke.sk

<u>Abstract.</u> The paper presents the theoretical and practical aspects of the design and constructions of a high-frequency full-bridge series-parallel load resonant converter for arc welding. The converter with maximum output current of 150A and an output no-load voltage of 70V operating at frequency from 65kHz to 100kHz is designed. Soft switching for all power switches is achieved by using the non-dissipative snubbers. This converter minimises the size and weight of the magnetic components in the converter, reduces output current ripple and switching losses in semiconductor devices.

<u>Keywords:</u> Resonant converter, Arc welding, High frequency power converters, ZVS converter, Soft switching, DC/DC converter.

1. INTRODUCTION

In the recent years the power converters are often used in many arc welding applications. The size of magnetic components and capacitors depends on the operation frequency [1]. The high frequency operation of the converter minimises the size and weight of the converter and reduces output current ripples. In this paper a welding supply with high frequency inverter is described. The IGBTs are often utilised to achieve high frequency at high power applications. However, the high switching frequency results in increased switching losses in power semiconductor devices at turn-on and turn-off.

The full bridge series-parallel (LCC) resonant converter with capacitive snubbers working above resonance frequency is used in the arc welding supply. The soft switching techniques is used in this converter. Zero-voltage switching for all power switches is ensured to reduce switching losses and achieve high efficiency in full load range and wide range of the output voltage. The optimal design of the resonant components L_S , C_S , C_P is very important for correct operation of the welding supply. The major advantages of the mentioned converter are low switching losses, good adaptation to various operating conditions, fast response, high efficiency, and improved power factor [10].

2. POWER AND CONTROL CIRCUITS

The simplified scheme of the LCC resonant converter as a power supply for arc welding is shown in Fig. 1. The converter consist of the input full bridge uncontrolled rectifier, input capacitive filter, full bridge IGBT inverter, series-parallel resonant components, high frequency high power coaxial transformer, center tapped high frequency rectifier with fast recovery diodes and output inductive filter. The resonant tank comprises three elements: series inductor L_S , series capacitor C_S and parallel capacitor C_P .

The capacitor C_S with the inductance L_S present a series resonant circuit. The capacitor C_P is connected in parallel with the transformer primary winding and they represent parallel resonant circuit. The IGBT's of the converter operate with variable switching frequency above resonance frequency in full operating range, hence the power switches are turned-on under zero voltage (ZVS).

In order to reduce turn-off losses of the switches to acceptable level the external capacitors $C_1...C_4$ (acting as a non-dissipative snubbers) are required. The converter transistors are operated with reduced switching losses, hence the switching frequency can be higher as in conventional hard-switching converters.

For welding process, the maximum arc voltage is about 35V and the voltage needed to a good arc ignition, at no-load, must be around 70V [4]. These conditions can be achieved using correct design of power circuits and suitable control. The short circuit current and the maximum load voltage must be limited and controlled.

In Shielded Metal Arc Welding (SMAW), which is the most popular welding process, the dc current must be controlled, The control scheme for the resonant converter is shown in Fig. 2. A microcomputer control or an integrated control circuit (e.g. UC 1861) can be utilised for the control of the converter. These circuits includes several functions needed to ensure correct welder behaviour in all operating conditions.

The microcomputer control system consist of the microprocessor, timer, multiplexer, analog-digital converter, output logic circuit, dead time generator and comparators. This control circuit is more difficult as integrated control circuit but its facilities are larger.

The resonant-mode power supply controller UC 1861 offers many features such as error amplifier, voltage controlled oscillator, one shot timing generator with zero crossing detection comparator, steering logic to two output drivers, a 5V bias generator, and undervoltage lockout. A latched fault management scheme provides soft start, restart delay, and a precision reference.



Fig. 1. Full-bridge series-parallel resonant converter for arc welding

The both resonant-mode control circuits are completely galvanically separated from the power circuits.

The resonant inverter working above the resonance frequency requires controlled switch-off times to ensure zero-voltage switching. The zero switch voltage needs to be sensed for both switches T_1 , T_4 in the arm and translate through sensing transformers to the zero input of the control circuit VS₁, VS₂.

The output current is sensed by a special current transformer. The rectified voltage from the current transformer CS_1 is fed into the non inverting input of the error amplifier as a feedback.

The output voltage is sensed by a voltage transformer and the rectified voltage VS₅ from transformer is fed into the comparator with hysteresis. If the output voltage v_0 is greater than the maximum value V_{Omax} the switching frequency is adjusted on the maximum value, thus the minimum value of the output voltage is achieved. The control circuits ensure that the welding process starts with maximum frequency.

Regulation is achieved by comparing actual voltage, which is proportional to the output current against reference voltage. Any changes of the output current due to load variations cause the pulse frequency change according to load and line conditions, stabilising the output current. The current transformer CS_2 is used by sensing the current in the resonant tank for overcurrent protection of the transistors. The high voltage MOS and IGBT gate drivers SKHI 20 are used to drive IGBTs.

3. DESIGN OF THE CONVERTER

The simplified model of the LCC-type series-parallel resonant converter used for analysis is shown in Fig. 3. For simplicity assume that a filter inductor L_L (see Fig. 1.) ensures that output current I_O is fully smoothed. It is also assumed that all components and devices of the converter are ideal. The load is presented by an equivalent resistance R_1 [3].



Fig. 3. The equivalent circuit

According to Fig. 3. the state-space model describing the dynamic behaviours of this converter is:



Fig. 2. Control circuit

$$\frac{d}{dt} \begin{bmatrix} v_{CP} \\ v_{CS} \\ i_R \end{bmatrix} = \begin{bmatrix} 0 & 0 & \frac{l}{C_P} \\ 0 & 0 & \frac{l}{C_S} \\ -\frac{l}{L_S} & -\frac{l}{L_S} & 0 \end{bmatrix} \begin{bmatrix} v_{CP} \\ v_{CS} \\ i_R \end{bmatrix} + \\ + \begin{bmatrix} 0 \\ 0 \\ \frac{l}{L_S} \end{bmatrix} (\pm V_S) + \begin{bmatrix} -\frac{l}{C_P} \\ 0 \\ 0 \end{bmatrix} i_l = A.x + b.v + e.z$$
(1)

The input voltage is a square wave whose rms value is:

$$V_R = \frac{4V_S}{\pi \sqrt{2}} \tag{2}$$

and the resonant components L_{S} and C_{S} can be calculates as follows:

$$L_S = \frac{Z_R}{2.\pi . f_{RS}} \tag{3}$$

$$C_{S} = \frac{l}{L_{S} \cdot (2\pi \cdot f_{RS})^{2}}$$
(4)

where the Z_{RS} is a characteristic impedance:

$$Z_{RS} = \sqrt{\frac{L_S}{C_S}} \tag{5}$$

whose value is found from[5]:

$$Z_{RS} = \frac{V_S . f_{RS}}{2 . I_{RS} . (f_S - f_{RS})} \tag{6}$$

where the I_{RS} is the resonant current at the short circuit :

$$I_{R_{RMS}} = \frac{2\sqrt{2}.I_O}{\pi.n} \tag{7}$$

The switching frequency f_S at no-load and short circuit has to be higher than resonant frequency f_R :

$$f_{S} > f_{RO} = \frac{l}{2\pi \cdot \sqrt{L_{S} \cdot \frac{C_{S} \cdot C_{P}}{C_{S} + C_{P}}}} > f_{RS} = \frac{l}{2\pi \cdot \sqrt{L_{S} \cdot C_{S}}}$$
(8)

The f_{RS} is the resonant frequency in the short circuit and the f_{RO} is the resonance frequency at no-load.

The magnitude of the voltage across the resonant capacitor C_P is:

$$V_{CpMAX} = \frac{\sqrt{2.V_R}}{\left[\left(1 + \frac{C_P}{C_S}\right)^2 \cdot \left[1 - \left(\frac{f_S}{f_R}\right)^2\right]^2 + \left[\mathcal{Q}_R \cdot \left(\frac{f_S}{f_R} - \frac{f_R}{f_S} \cdot \frac{C_P}{C_P + C_S}\right)\right]^2\right]}$$
(9)

where Q_R is the quality factor and it is defined as:

$$Q_R = \frac{Z_R}{R_I} \tag{10}$$

At no-load when the output voltage is maximum (in this application about 70V) the quality factor Q_R can be neglected.

The normalised peak voltage across parallel capacitor C_P and parallel output resistor R_1 according equation (9) is illustrated in Fig. 4. for the capacitance ratio $C_P/C_S=1$ and 0,5. It is plotted as a function of the ratio f_S/f_{RO} at selected value of Q_R . From Fig. 4. we can find the value of parallel resonant capacitor C_P . The Fig. 5. shows normalised peak voltage across series capacitor C_S as function of f_S/f_{RO} at selected value of Q_R and C_P/C_S .

Fig. 5. shows normalised resonant current $I_{RM}.Z_O/V_R.\sqrt{2}$ versus f_S/f_{RS} . It can be seen that high values of the I_{RM} occur at the resonant frequency of the short circuit f_{RS} . The resonant current increases with increasing output resistance R_1 or decreasing Q_R .

The choice of the parallel capacitor C_P has to be a compromise between the significant various of the inverter frequency, the output voltage and high resonant current flowing at no load.

The following parameters of resonant components were obtained: $L_s=42\mu$ H, $C_s=380$ nF, $C_P=380$ nF for short circuit resonance frequency 42kHz, minimum switching frequency 65kHz and maximum switching frequency 100kHz.

The operating conditions similar to those in the conventional electric arc welders are ensured by the proper design of the control circuits. By using a small value of the parallel capacitance C_P the inverter resonant current increased to higher values than at the short circuits. This nuisance we can removed. The control algorithm recognises two states of the system according to the load current.

The working state is recognised at $i_O > I_{Omin}$ where I_{Omin} is the minimum load current suitable for arc welding. No-load is recognised at $i_O < I_{Omin}$. At the working state the control algorithm adjusts the frequency according to difference between output current and reference signal. If the no-load is recognised the maximum frequency is set.







Fig. 4. Normalized maximum voltage Fig. 5. Normalized maximum voltage Fig. 6. Normalized maximum current across the parallel capacitor f_S/f_{RO} for versus different values of Q_R and C_P/C_S

acrooss the series capacitor versus f_S/f_{RO} for different values of Q_R and C_P/C_S

4. EXPERIMENTAL RESULTS

The measurement was made at voltage V = 300V across the input filter C_{F1}. The rated output current of 150A was reached at arc voltage. The output voltage at no-load circuit is from 40V to 70V. The value of the welding current is set by reference voltage fluently from 40A up to 150A. Waveforms of the voltage across resonant tank v_R and resonant current i_R are displayed in Fig. 7. Fig. 8. shows the switch voltage v_{CE} and switch current i_C . The converter operates above resonance frequency, hence the power switches are turned-on and turned-off under zero voltage switching (ZVS). We can see waveforms of the output current io and voltage vo during full arc welding process in Fig. 9. At no-load the converter is driven to frequency 100kHz and the voltage across the load is about 70V. At short circuit the minimum output current is about 80A at 100kHz. The current of the arc is about 50A.

Fig. 7. Resonant voltage across resonant tank v_R and resonant current i_R . t: 5µs/div, v_R : 100V/div, i_R : 20A/div

Fig. 8. Switch voltage v_{CE} and switch current i_C . t: 2.5µs/div, v_{CE}: 50V/div, i_C: 5A/div

Fig. 9. Output voltage v_o and output current i_o during welding. t: 100ms/div, vo: 50V/div, io: 20A/div

5. CONCLUSION

The dc-dc converter with series-parallel load resonant inverter is used as the arc welding supply. The LCC-type load resonant tank was chosen due to its ability to operate at high frequency and together with control circuits limit voltage and current under open and short circuit conditions, respectively. A full design procedure for arc welding application has been developed and two control circuits for this converter are presented. The theoretical characteristics of the peak stresses are plotted in the normalised output plane.

The dynamical and steady-state properties of the dc-dc converter working at switching frequency from 65kHz to 100kHz with output current from 50A to 150A, output voltage at no-load voltage of 70V was presented.

The presented dc-dc converter is suitable for arc welding source for its small weight and size, good efficiency and fast response.

6. ACKNOWLEDGEMENT

This work has been supported by Grand Agency of the Slovak Republic under the contact VEGA 1/6110/99.

7. REFERENCES

- [1] Pollock,H., Flower,J.,O.: Series-Parallel Load-Resonant Converter for Controlled-Current Arc Welding Power Supply, IEE Proc. Electr. Power Appl., Vol. 143, No. 3, May 1996, pp.211-218.
- [2] Reinold,H., Steiner,M.: Characterization of Semiconductor Losses in Series Resonant DC-DC Converters for High Power Applications Using Transformers with Low Leakage Inductance, EPE'99, 1999, Lausanne, pp.10.
- [3] Cheng,J.,H., Witulski,A.,F.: Analytic Solutions for LLCC Parallel Resonant Converter Unify the Design and Analysis of Two- and Three-Element Converters, Proceed. of the IEEE PESC'96, 1996, pp. 266-271.
- [4] Mecke,H., Fischer,W., Werther,F.: Soft Switching Inverter Power Source for Arc Welding. EPE'97, 1997, Trondheim, Vol. 4, pp. 333-337.
- [5] Malesani,L., Mattaveli,P., Rosetto,L., Tenti,P., Marin,W., Pollman,A.: Electronic Welder with High-Frequency Resonant Inverter. IEEE Trans. on Industry Appl., Vol.1, No. 2/1995, pp. 273-279.
- [6] Bhat,A.,K.,S., Dewan,S.,B.: Analysis and Design of a High-Frequency Resonant Converter Using LCC-Type Commutation. IEEE Trans. on Power Electronics, Vol. PE-2, No. 4, October 1987, pp. 291-300.
- [7] Trip,N.,D., Popescu,V.: Analysis and Experimental Results of a Resonant Converter with a Series Load and Phase Control, EDPE'99, 1999, High Tatras, pp. 486-490.

- [8] Grzesik, B., Kaczmarczyk, Z., Kasprzak, M.: Integral Pulse Modulation - New Strategy for Series Resonant Half Bridge Inverter of Class D and DE, EDPE'99, 1999, High Tatras, pp. 100-104.
- [9] Rácek, V., Flajzík, P.: Resonant Converter with IGBT Transistors and a Switching Frequency of 100kHz, EDPE'96, 1996, High Tatras, pp. 50-57.
- [10] Dudrík, J., Dzurko, P.: Series-Parallel Resonant DC-to-DC Converter for Arc Welding, PEMC'98, Praha, 1998, pp. 8/16-20.

THE AUTHORS

Peter Dzurko was born in Královský Chlmec, Slovakia, in 1974. He received the Ing. (MSc) degree in electrical engineering from the Technical University of Košice in 1997. Currently he is a PhD student in Electrical Engineering at the Department of Electrical Drives and Mechatronics at the Technical

University of Košice. His research interests are dc-to-dc converters, especially high power switching converters and their computer simulations.

Jaroslav Dudrík was born in Košice, Slovakia in 1952. He received the MSc and PhD degree in electrical engineering from the Technical University of Košice in 1976 and 1987. He is an Associate Professor of Electrical Engineering at the Technical University of Košice, where he is engaged in teaching and research. His primary

interest is power electronics. His research includes dc-to-dc converters, high power soft switching converters and control theory of converters.

Peter Višnyi was born in 1954 in Bratislava, Slovakia. He received the Ing (MSc) and PhD degree from the Technical University of Košice in 1978 and 1983, respectively. He works as a research worker at the Department of Electrical Drives and Mechatronics of the Technical University of Košice. He is

specialised in digital control of power converters and electrical drives.